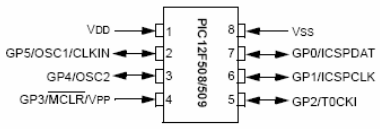
Programmer PIC-01
How to program a PIC10Fxxx on PIC12Cxxx 8 pins support

Correspondence array:

Signal	Support 8 pins PIC-01 PIC12Fxx	Correspondence PIC10Fxx
Vpp	4	8
Vdd	1	2
Vss	8	7
DATA	7	5
CLK	6	4

Broaching of PIC12Fxxx :

PDIP, SOIC, MSOP



1.1 Hardware Requirements

The PIC12F508/509 requires one power supply for VDD (5.0V) and one for VPP (12V).

TABLE 1-1: PIN DESCRIPTIONS (DURING PROGRAMMING): PIC12F508/509

Pin Name	During Programming		
	Function	Pin Type	Pin Description
GP1	ICSPCLK	I	Clock input – Schmitt Trigger input
GP0	ICSPDAT	I/O	Data input/output – Schmitt Trigger input
MCLR/VPP	Program/Verify mode	P(1)	Program Mode Select
VDD	VDD	P	Power Supply
VSS	VSS	P	Ground

Legend: I = Input, O = Output, P = Power

Note 1: In the PIC12F508/509, the programming high voltage is internally generated. To activate the Program/ Verify mode, high voltage of IIHH current capability (see Table 6-1) needs to be applied to the MCLR input.

Broaching of PIC10Fxxx

DIP **DIP**

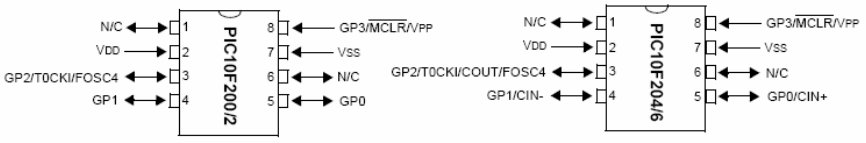


TABLE 1-1: PIN DESCRIPTIONS (DURING PROGRAMMING): PIC10F200/202/204/206

Pin Name	During Programming		
	Function	Pin Type	Pin Description
GP1	ICSPCLK	I	Clock input – Schmitt Trigger input
GP0	ICSPDAT	I/O	Data input/output – Schmitt Trigger input
MCLR/VPP	Program/Verify mode	P(1)	Program Mode Select
VDD	VDD	P	Power Supply
VSS	VSS	P	Ground

Legend: I = Input, O = Output, P = Power

Note 1: In the PIC10F200/202/204/206, the programming high voltage is internally generated. To activate the Program/Verify mode, high voltage of IIHH current capability (see Table 6-1) needs to be applied to the MCLR input.